

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/615,500	ADILETTA ET AL.	
		Examiner	Art Unit	Page 1 of 1
		Richard Ellis	2183	

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,452,452	09-1995	Gaetner et al.	718/103
*	B	US-6,938,147	08-2005	Joy et al.	712/28
*	C	US-5,475,856	12-1995	Kogge, Peter M.	712/20
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Agarwal, Anant et al., APRIL: A Processor Architecture for Multiprocessing, MIT Document Services, MIT-LCS-TM-450, June 1, 1991.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.